

IN THE CLAIMS:

Amend the claims as indicated below.

Claims 1-29 (canceled).

1 30. (previously presented) A method for processing a spread spectrum signal,
2 comprising:
3 receiving a code modulated spread spectrum signal;
4 generating a code modulated signal replica of the received signal;
5 correlating the received signal with a first code phase delayed replica to generate an early
6 correlation product;
7 correlating the received signal with a second code phase delayed replica to generate a
8 prompt correlation product;
9 correlating the received signal with a third code phase delayed replica to generate a late
10 correlation product; and
11 detecting a multipath error in the received signal based on at least one relationship
12 between an amplitude of the early correlation product, an amplitude of the prompt correlation
13 product and an amplitude of the late correlation product.

1 31. (previously presented) The method of claim 30, wherein detecting the multipath error
2 comprises detecting at least one of:
3 power of respective early, prompt and late correlation products;
4 phase of respective early, prompt and late correlation products; and
5 quadrature of respective early, prompt and late correlation products.

1 32. (currently amended) The method of claim 30 wherein detecting the multipath error
2 comprises ~~comprise~~:

3 determining a sign of the multipath error; and
4 determining a relative magnitude of the multipath error.

1 33. (previously presented) The method of claim 32, wherein determining a sign of the
2 multipath error comprises comparing a ratio of the amplitude of the prompt correlation product to
3 an equal amplitude of the early and late correlation products, wherein the early and late
4 correlation products are offset symmetrically from the prompt correlation product.

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1 34. (previously presented) The method of claim 33, wherein:
2 determining that a lag error exists when the amplitude of the prompt correlation product
3 is less than twice the equal amplitude;
4 determining that a lead error exists when the amplitude of the prompt correlation product
5 is more than twice the equal amplitude; and
6 determining that no multipath error exists if the amplitude of the prompt correlation
7 product is equal to twice the equal amplitude.

1 35. (previously presented) The method of claim 32, further comprising, when the relative
2 magnitude is determined to be below a predetermined value, determining a correction to a
3 pseudorange calculation, wherein the correction is proportional to a sum of the amplitudes of the
4 early correlation product and the late correlation product divided by the prompt correlation
5 product.

1 36. (previously presented) The method of claim 30, further comprising using the
2 multipath error to refine a position calculation made by a receiver processor.

1 37. (previously presented) The method of claim 30, further comprising using the
2 multipath error to adjust a phase of the code modulated signal replica such that phases of
3 respective early, prompt and late correlation products are adjusted.

1 38. (previously presented) The method of claim 30, further comprising using the
2 multipath error to generate a control signal to control a phase relationship between respective
3 early and late correlation products and further to control a relationship of the prompt correlation
4 product with respect to the early correlation product and the late correlation product.

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1 39. (previously presented) The method of claim 30, further comprising using the
2 multipath error to generate a signal model of an interfering multipath signal.

1 40. (previously presented) The method of claim 30, further comprising using the signal
2 model for multipath signal cancellation.

1 41. (previously presented) An apparatus for receiving a signal, the apparatus comprising:
2 a receiver configured to receive a signal comprising a PN code;
3 a code generator coupled to the receiver, the code generator configured to generate a PN
4 code replica of the received signal;
5 an error detector coupled to the receiver and to the code generator, wherein the error
6 detector detects code phase error in the received signal based on at least one relationship between

7 a first correlation product of an early code phase delayed version of the code replica and the
8 received signal, a second correlation product of a prompt code phase delayed version of the code
9 replica and the received signal, and a third correlation product of a late code phase delayed
10 version of the code replica and the received signal.

1 42. (previously presented) The apparatus of claim 41, further comprising at least one
2 detector coupled to receive the first correlation product, the second correlation product and the
3 third correlation product.

1 43. (previously presented) The apparatus of claim 42, wherein detecting the code phase
2 error comprises detecting at least one of:

3 power of respective first, second and third correlation products;
4 phase of respective first, second and third correlation products; and
5 quadrature of respective first, second and third correlation products.

1 44. (previously presented) The apparatus of claim 43, further comprising a code phase
2 error detector coupled to the at least one detector and configured to determine a sign of the code
3 phase error and a relative magnitude of the code phase error.

1 45. (previously presented) The apparatus of claim 44, wherein determining a sign of the
2 code phase error comprises the code phase error detector comparing a ratio of the amplitude of
3 the second correlation product to an equal amplitude of the first and third correlation products,
4 wherein the first and third correlation products are offset symmetrically from the second
5 correlation product.

1 46. (previously presented) The apparatus of claim 45, wherein the code phase error
2 detector is further configured to:
3 determine that a lag error exists when the amplitude of the second correlation product is
4 less than twice the equal amplitude;
5 determine that a lead error exists when the amplitude of the second correlation product is
6 more than twice the equal amplitude; and
7 determine that no code phase error exists if the amplitude of the second correlation
8 product is equal to twice the equal amplitude.

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1 47. (previously presented) The apparatus of claim 44, wherein the code phase error
2 detector is further configured to determine a correction to a pseudorange calculation when the
3 relative magnitude is determined to be below a predetermined value, wherein the correction is
4 proportional to a sum of the amplitudes of the first correlation product and the third correlation
5 product divided by the second correlation product.

1 48. (previously presented) The apparatus of claim 41, wherein the receiver is coupled to
2 receive a code phase error signal from the code phase error detector, and wherein the receiver is
3 configured to use the code phase error signal to refine a position calculation.

1 49. (previously presented) The apparatus of claim 41, further comprising an adjustable
2 delay element coupled to the code generator and further coupled to the code phase error detector
3 to receive a code phase error signal, wherein the at adjustable delay element is configured to use
4 the code phase error signal to adjust a phase of the PN code replica such that phases of respective

5 first, second and third correlation products are adjusted.

1 50. (previously presented) The apparatus of claim 41, further comprising at least one
2 delay element coupled to at least one correlator and further coupled to the code phase error
3 detector, wherein the at least one delay element is configured to use the code phase error signal to
4 generate a control signal to control a phase relationship between respective first and third
5 correlation products and further to control a relationship of the second correlation product with
6 respect to the first correlation product and the third correlation product.

1 51. (previously presented) The apparatus of claim 41, further comprising a signal model
2 element coupled to the code phase error detector, wherein the signal model element is configured
3 to use the code phase error signal to generate a signal model of an interfering code phase signal.

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1 52. (previously presented) The apparatus of claim 41, further comprising means for using
2 the signal model for code phase signal cancellation.

1 53. (previously presented) An apparatus for processing global positioning system (GPS)
2 satellite signals, the apparatus comprising:

3 receiver means for receiving a GPS signal;

4 code generator means for generating a PN code replica of the received GPS signal; and

5 multipath error detector means for detecting a multipath error in the received GPS signal

6 based on at least one relationship between a first correlation product of the received GPS signal

7 with a first PN code phase delayed replica, a second correlation product of the received GPS

8 signal with a second PN code phase delayed replica, and a third correlation product of the

9 received GPS signal with a third PN code phase delayed replica.

1 54. (previously presented) The apparatus of claim 53, further comprising at least one
2 detector means coupled to receive the first correlation product, the second correlation product
3 and the third correlation product.

1 55. (previously presented) The apparatus of claim 54, wherein detecting the multipath
2 error comprises detecting at least one of:

3 power of respective first, second and third correlation products;

4 phase of respective first, second and third correlation products; and

5 quadrature of respective first, second and third correlation products.

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cont 1 56. (previously presented) The apparatus of claim 55, wherein the multipath error
2 detector means is coupled to the at least one detector means and is configured to determine a sign
3 of the multipath error and a relative magnitude of the multipath error.

1 57. (previously presented) The apparatus of claim 56, wherein determining a sign of the
2 multipath error comprises the multipath error detector means comparing a ratio of the amplitude
3 of the second correlation product to an equal amplitude of the first and third correlation products,
4 wherein the first and third correlation products are offset symmetrically from the second
5 correlation product.

1 58. (previously presented) The apparatus of claim 57, wherein the multipath error
2 detector means is further configured to:
3 determine that a lag error exists when the amplitude of the second correlation product is

4 less than twice the equal amplitude;
5 determine that a lead error exists when the amplitude of the second correlation product is
6 more than twice the equal amplitude; and
7 determine that no multipath error exists if the amplitude of the second correlation product
8 is equal to twice the equal amplitude.

1 59. (previously presented) The apparatus of claim 56, wherein the multipath error
2 detector means is further configured to determine a correction to a pseudorange calculation when
3 the relative magnitude is determined to be below a predetermined value, wherein the correction is
4 proportional to a sum of the amplitudes of the first correlation product and the third correlation
5 product divided by the second correlation product.

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1 60. (previously presented) The apparatus of claim 54, wherein the receiver means is
2 coupled to receive a multipath error signal from the multipath error detector means, and wherein
3 the receiver means is configured to use the multipath error signal to refine a position calculation.

1 61. (previously presented) The apparatus of claim 54, further comprising an adjustable
2 delay means coupled to the code generator means and further coupled to the multipath error
3 detector means to receive a multipath error signal, wherein the at adjustable delay element is
4 configured to use the multipath error signal to adjust a phase of the PN code replica such that
5 phases of respective first, second and third correlation products are adjusted.

1 62. (previously presented) The apparatus of claim 54, further comprising at least one
2 delay means coupled to at least one correlator and further coupled to the multipath error detector

3 means, wherein the at least one delay means is configured to use the multipath error signal to
4 generate a control signal to control a phase relationship between respective first and third
5 correlation products and further to control a relationship of the second correlation product with
6 respect to the first correlation product and the third correlation product.

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1 63. (previously presented) The apparatus of claim 54, further comprising a signal model
2 means coupled to the multipath error detector means, wherein the signal model means is
3 configured to use the multipath error signal to generate a signal model of an interfering multipath
4 signal.

1 64. (previously presented) The apparatus of claim 54, further comprising means for using
2 the signal model for multipath signal cancellation.
